



Docket No.: 42390P6725

**RESPONSE UNDER 37 C.F.R. 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2187**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Altug Koker et al.

Application No.: 09/372,296

Filed: August 11, 1999

For: REDUCING MEMORY ACCESS
LATENCIES FROM A BUS USING
PRE-FETCHING AND CACHING

Examiner: Jasmine Song

Art Group: 2187

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AMENDMENT AND RESPONSE UNDER 37 CFR §1.116

BOX AF
Assistant Commissioner for Patents
Washington, DC 20231-9998

Sir:

In response to the outstanding Office Action mailed July 18, 2001. Please consider the following remarks:

IN THE CLAIMS

Following is a complete set of claims.

1. (AMENDED) A method comprising:
pre-fetching a plurality of data from a memory to a cache queue in response to a request;

and